Docket No.: 2013P134

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

HOKYUN AHN, ET AL.

Application No.:

Filed:

For: **SEMICONDUCTOR DEVICE HAVING**

T-SHAPED GATE ELECTRODE AND METHOD OF MANUFACTURING THE

SAME

Art Group:

Examiner:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

-l- 2013P134

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

12400 Wilshire Boulevard, 7th

Date: November 25, 2003

Los Angeles, CA 90025 Telephone: (310) 207-3800

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Madya Gordon Nadya Gordon

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				Examiner Name		
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U.S. PATENT DOCUMENTS								
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Initials*	Examiner Cite No.1	Number - Kind Code ² (if known)	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
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FOREIGN PATENT DOCUMENTS								
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		JP 10-150054	02-06-1998	Oshima Tomoyuki				
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STATEMENT BY APPLICANT (use as many sheets as necessary)				First Named Inventor	Hokyun Ahn		
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		D. Fanning, L. Witkowski, J. Stidham, HQ. Tserng, M. Muir and P. Saunier, Dielectrically defined optical T-gate for high power GaAs pHEMTs, Tri-Quint Semiconductor Texas, GaAs Mantech Conference, April 8-11, 2002, San Diego, California.			
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